



FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 174/221	APPLICATION NO. 09/924,274
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT Paul Metzgen	CONFIRMATION NO. 4898
		FILING DATE August 7, 2001	GROUP <del>2422</del> 2825

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
V/S	5,068,823	11/26/91	Robinson	395	500	
A	5,142,625	08/25/92	Nakai	395	275	
	5,535,342	07/09/96	Taylor	395	307	
	5,548,228	08/20/96	Madurawe	326	41	RECEIVED
	5,684,980	11/04/97	Casselman	395	500	APR 23 2003
	5,966,534	10/12/99	Cooke et al.	395	705	
	5,968,161	10/19/99	Southgate	712	37	Technology Center 2100
	6,085,317	07/04/00	Smith	713	1	
V/S	6,282,627	08/28/01	Wong et al.	712	15	

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
V/S	EP 0 419 105 A2	03/27/91	EPO	G06F	15/78		
	EP 0 419 105 A3	03/27/91	EPO	G06F	15/78		
	EP 0 445 913 A2	09/11/91	EPO	G06F	15/60		
	WO 94/10627	05/11/94	PCT	G06F	5/00		
	EP 0 759 662 A2	02/26/97	EPO	H03K	19/177		
	WO 97/09930	03/20/97	PCT	A61B	8/00		
	WO 97/13209	04/10/97	PCT	G06F	17/50		
	EP 0 801 351 A2	10/15/97	EPO	G06F	13/12		
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	EP 0 829 812 A2	03/18/98	EPO	G06F	17/50		
V/S	WO 00/38087	06/29/00	PCT	G06F	17/50		

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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V/S	Callahan, Timothy J. et al. "The Garp Architecture and C Compiler," Computer, April 2000, pp. 62-69.
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VUTHE SIEK

DATE CONSIDERED 2/13/06

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.

 <b>U.S. DEPARTMENT OF COMMERCE</b> <b>PATENT AND TRADEMARK OFFICE</b>	ATTY. DOCKET NO.	APPLICATION NO.
	174/221	09/924,274
	APPLICANT Paul Metzgen	CONFIRMATION NO. 4898
FILING DATE August 7, 2001	GROUP <u>2129 2828</u>	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

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VS	Edwards, M.D. et al. "Software acceleration using programmable hardware devices," January 1996, pp. 55-63.
	ELECTRONIK, DE, FRANZIS VERLAG GMBH - "MIT PROGRAMMIERBARER LOGIK VERHEIRATET," March 31, 1998, Vol. 47, No. 7, p. 38.
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VS	Wo, D. et al. "Compiling to the gate level for a reconfigurable co-processor," <u>Proceedings of IEEE Workshop</u> , April 10, 1994, Los Alamitos, CA, pp. 147-154.

All references have been considered

Examiner \_\_\_\_\_

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DATE CONSIDERED 2/13/05

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